

IN THE CLAIMS:

Please amend claim 1 as follows:

1. (Currently Amended) A liquid crystal display device comprising a first substrate and a second substrate which are arranged to face each other with a liquid crystal layer therebetween, wherein the first substrate includes a plurality of gate lines which extend in a first direction, a plurality of drain lines which extend in a second direction, and holding capacitance lines which extend in ~~[[a]]~~ the first direction, wherein
a pixel and a switching element are provided to a region which is surrounded by two neighboring gate lines out of the plurality of gate lines and two neighboring drain lines out of the plurality of drain lines,
the pixel includes a light transmitting region which allows light incident from a back surface of the first substrate to pass therethrough and a light reflecting region which allows light incident from the second substrate side to be reflected thereon,
the light transmitting region includes a first pixel electrode having the light transmitting property and the light reflecting region includes a second pixel electrode having the light reflecting property,
an insulation film and a holding capacitance electrode which extends in the second direction and is connected to one of the holding capacitance lines are provided below the second pixel electrode,
the holding capacitance electrode is formed on the same layer as the gate lines,
and
the holding capacitance electrode is formed in an overlapped manner to a boundary portion between the light transmitting region and the light reflecting region, and is formed of a material having a light shielding property.
2. (Original) A liquid crystal display device according to claim 1, wherein the first pixel electrode is formed over the holding capacitance electrode and a holding capacitance is formed by way of an anodized film formed over the holding capacitance electrode.
3. (Previously Presented) A liquid crystal display device comprising a first substrate and a second substrate which are arranged to face each other with a liquid crystal layer therebetween, a plurality of gate lines which are arranged in parallel on one of the first substrate and the second substrate, and a plurality of drain lines which are arranged to

cross respective gate lines of the plurality of gate lines and are arranged in parallel, wherein

regions which are surrounded by the gate lines and the drain lines constitute pixel regions, and each pixel region includes a switching element which is operated in response to a scanning signal applied from the gate line and a pixel electrode to which a video signal is supplied from the drain line through the switching element,

the pixel electrode is constituted of a first pixel electrode formed of a light transmitting conductive layer which is disposed in one light transmitting region defined in the pixel region and a second pixel electrode formed of a non-light transmitting conductive film which is disposed in another light reflecting region defined in the pixel region,

an insulation film is formed above the first pixel electrode and an opening which allows the first pixel electrode to be exposed is formed in a region of the insulation film corresponding to the light transmitting region,

the second pixel electrode is formed over the light reflecting region of the insulation film, and

a holding capacitance electrode is formed on the same layer as the gate line is arranged at a portion corresponding to a side wall surface of the opening of the insulation film, the holding capacitance electrode extends in parallel with the drain line, and the holding capacitance electrode is formed of a material having a light shielding property.